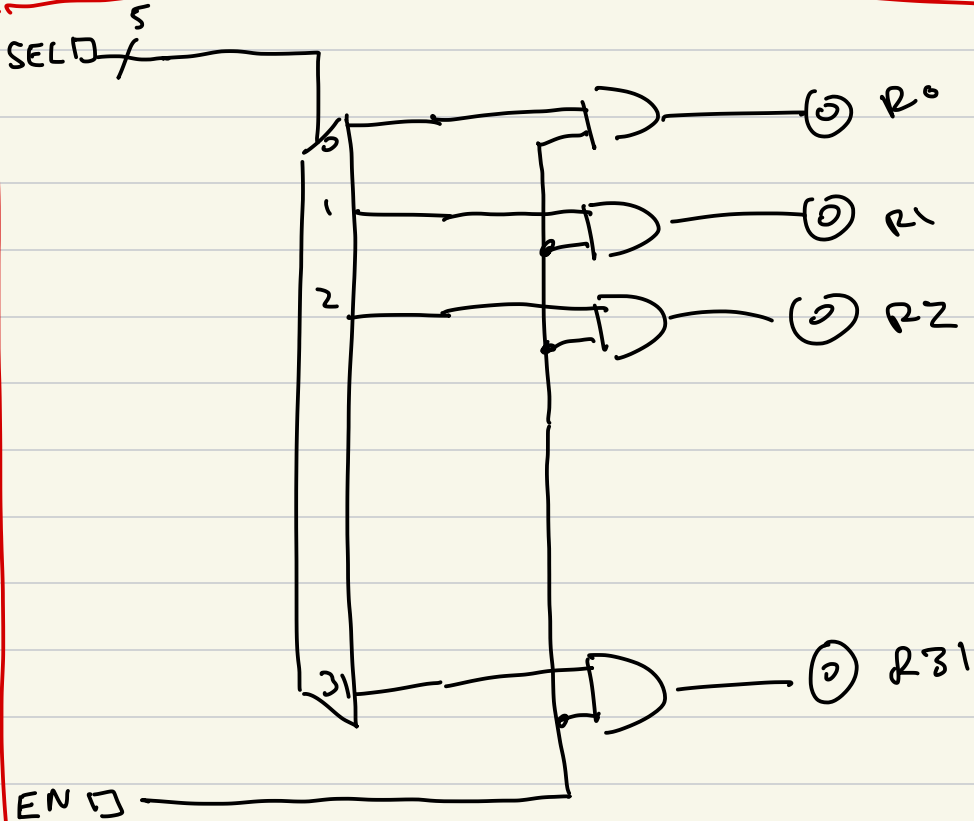
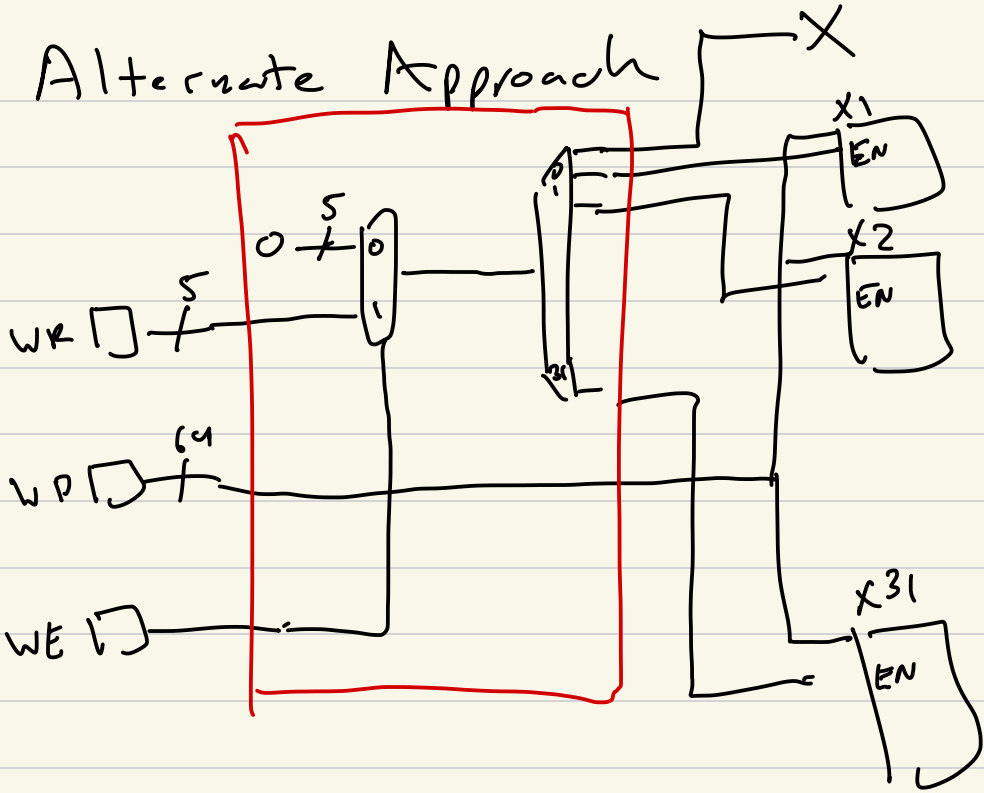


# C5315-02 Lab Processor Design ALU

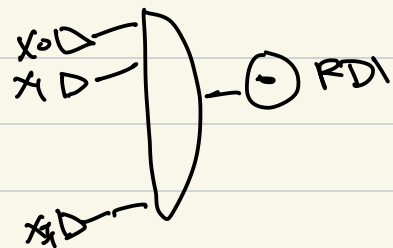
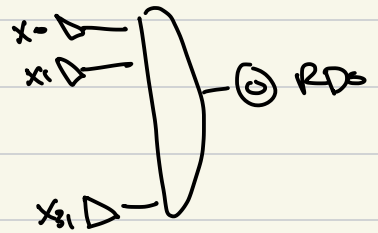
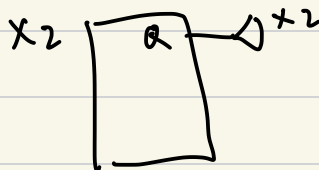
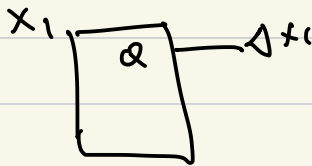
## Decoder with Enable



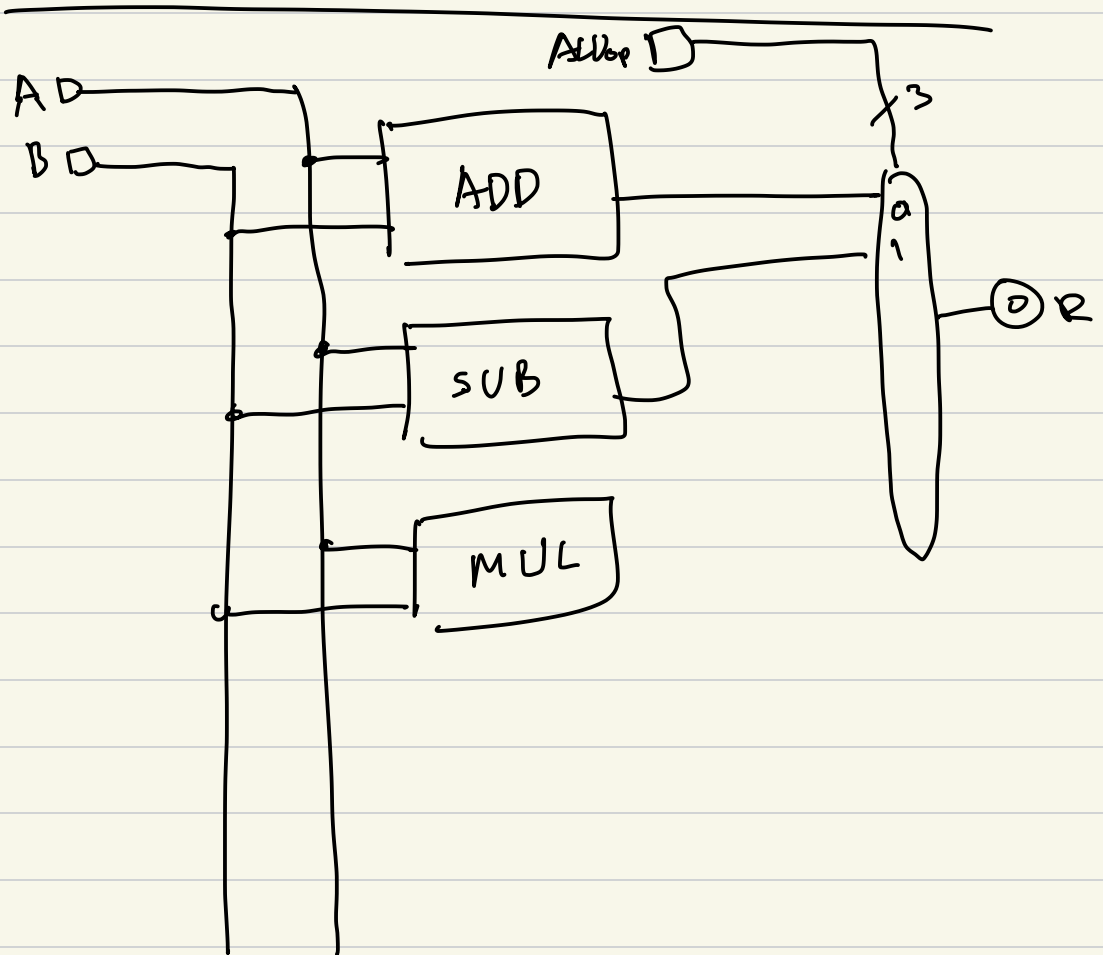
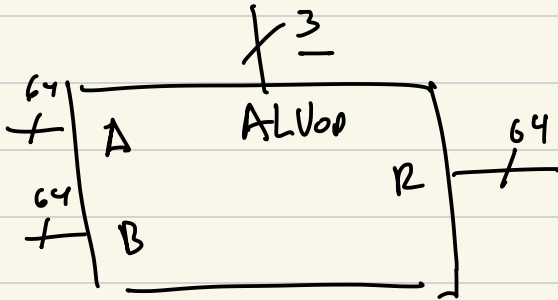
# Alternate Approach



$x_0 \quad 0 \rightarrow x_0$



# ALU Arithmetic Logic Unit



li to, 3

li to, 4

add to, to, 61